



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

EARNSHAW et al

Atty. Ref.: 550-506

Serial No. 10/751,108

Group:

Filed: January 5, 2004

Examiner:

For: MODELING INTEGRATED CIRCUITS

\* \* \* \* \*

June 7, 2004

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**INFORMATION DISCLOSURE STATEMENT**

Attention is invited to the literature and patent citations of record in the parent application Serial No. 09/328,420 where copies of these documents may be found.


Attached is the PTO-1449.

Official citation and consideration of all of these documents is requested. Please return to the undersigned a copy of the attached forms PTO-1449 with the examiner's initials in the left column [MPEP §609] with the next communication.

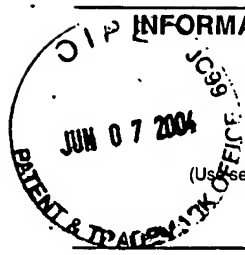
EARNSHAW et al  
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Respectfully submitted,

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**INFORMATION DISCLOSURE**  
**CITATION**

~~ATTY. DOCKET NO.~~

SERIAL NO.

550-506

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APPLICANT

EARNSHAW et al

FILING DATE

**GROUP**

January 5, 2004

## U.S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

[illegible]

**OTHER DOCUMENTS** (including Author, Title, Date, Pertinent pages, etc.)

	Challenges in Cross-Development, IEEE Micro, July 1997, R. Earnshaw et al., July 1997, pages 28-36.
	On the Average Delay for Routing Subject to Independent Deflections, Hajek, et al., IEEE Transactions on Information Theory, Vol. 39, Issue 1, Jan. 1993, pages 84-91.
	Design Simulation Models, ARM Product Information, ARM Limited
	United Kingdom Search Report dated November 19, 1998.

\*Examiner

Date Considered

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to application.

Form PTO-FB-A820 (Also PTO-1449)